

FIG. 1

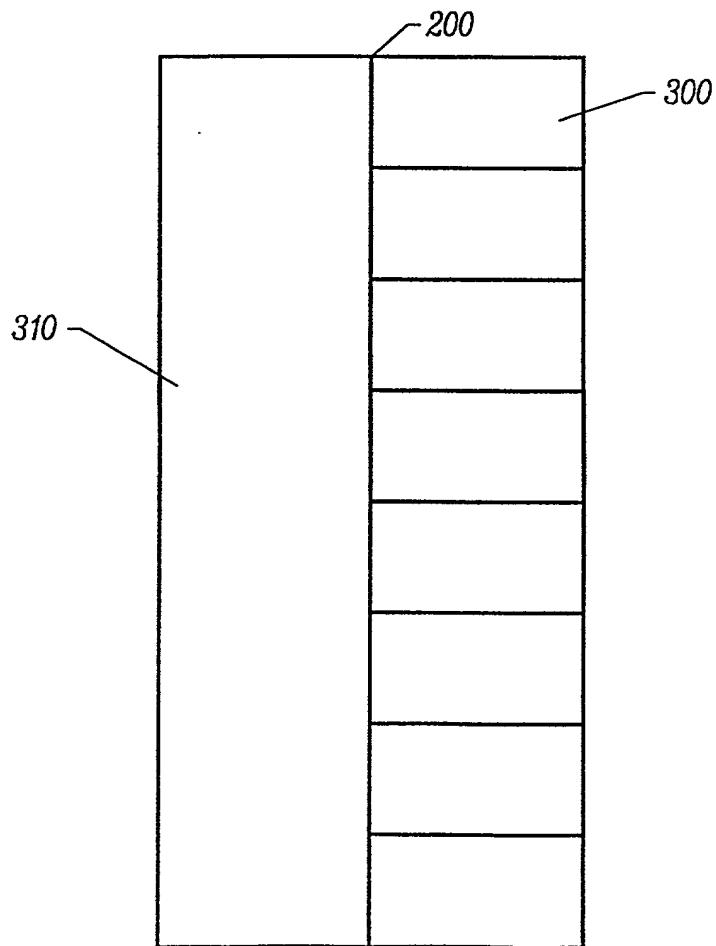


FIG. 3

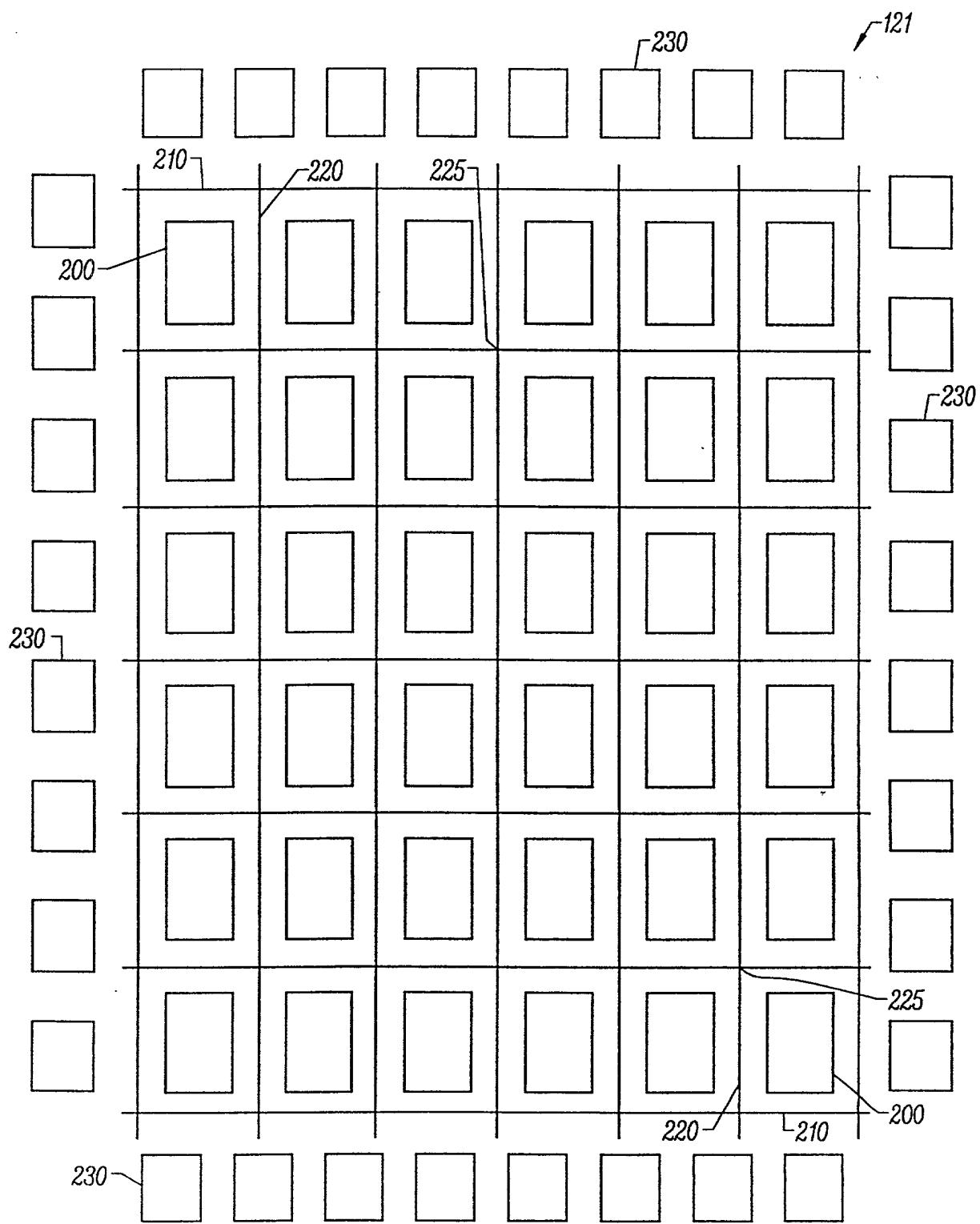


FIG. 2

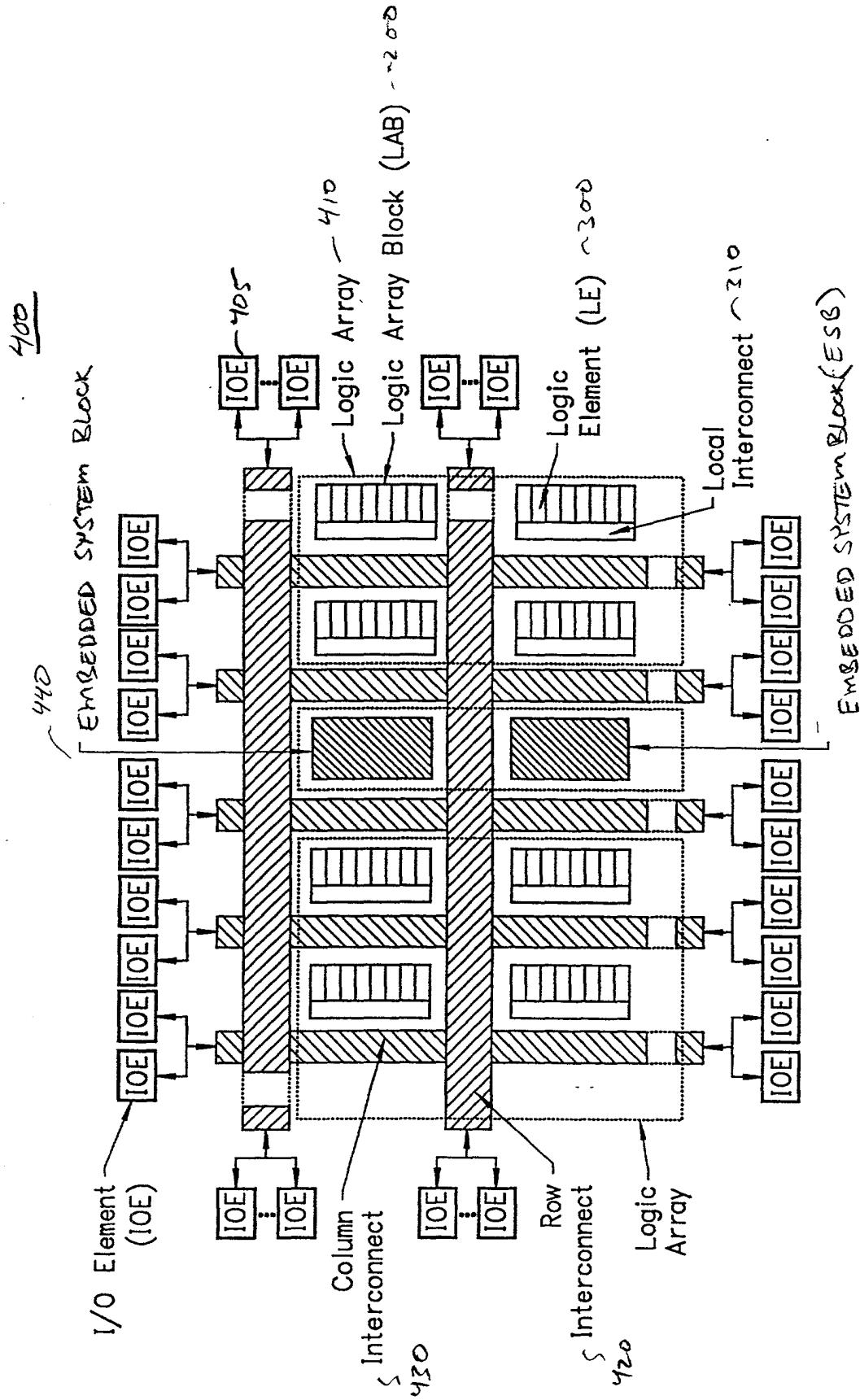


FIG. 4

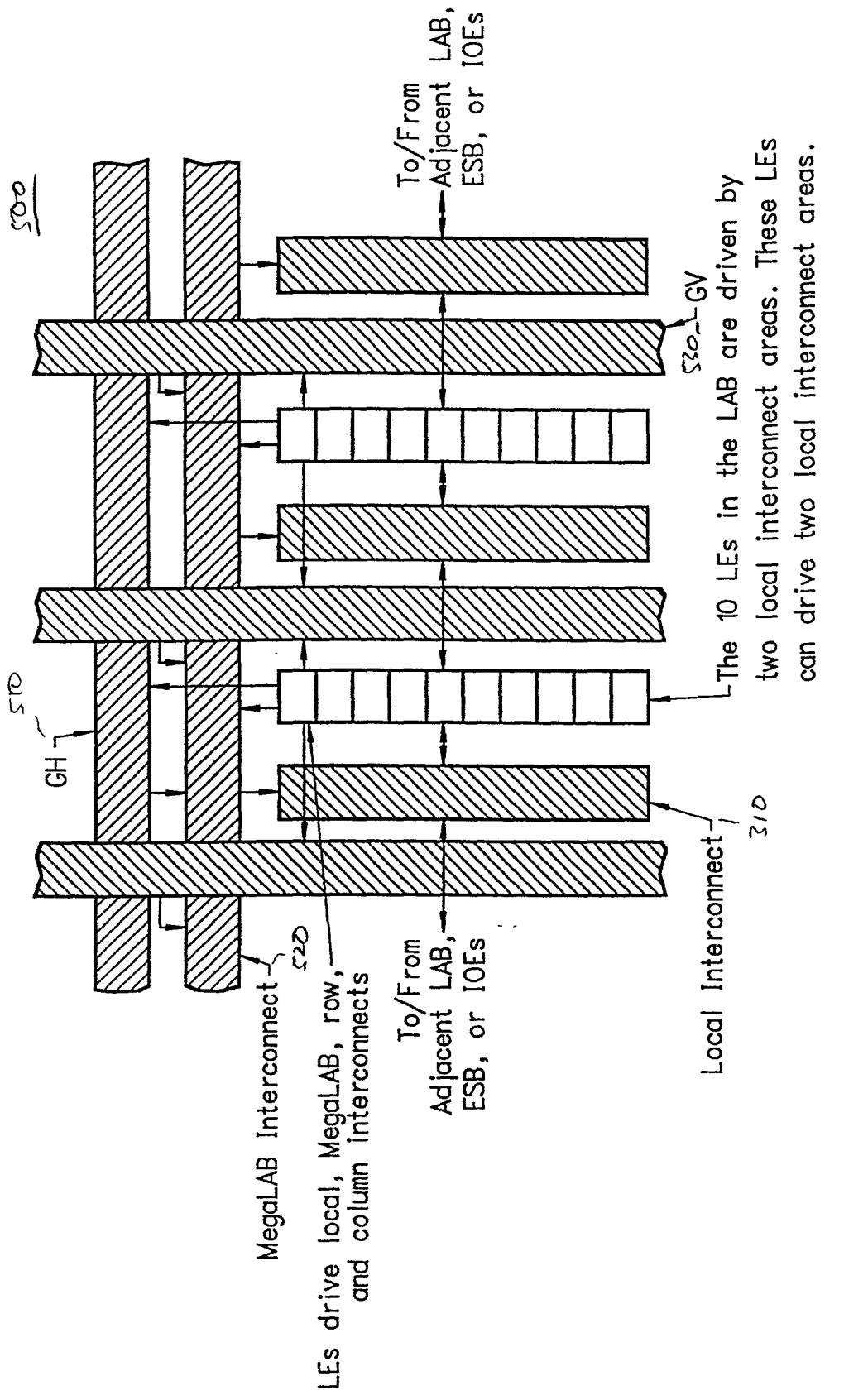


FIG. 5

600

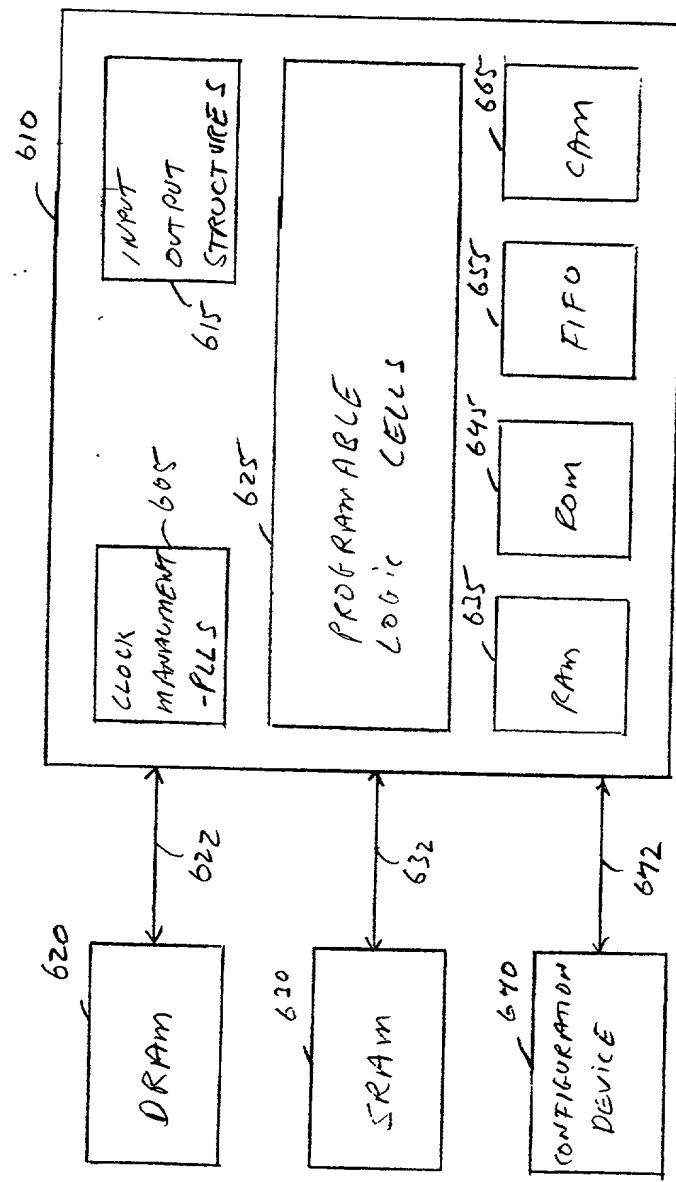


Figure 6

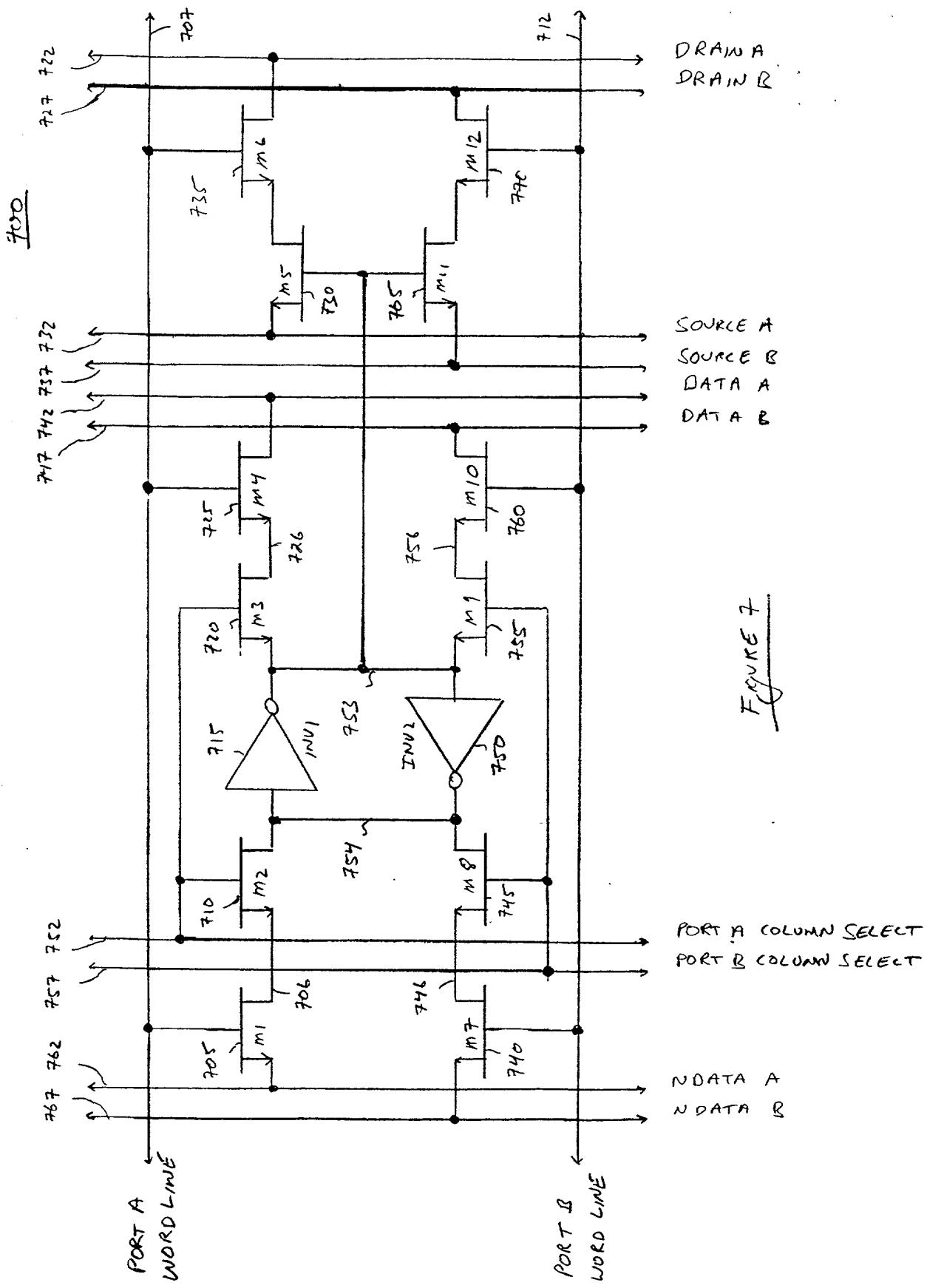


FIGURE 7

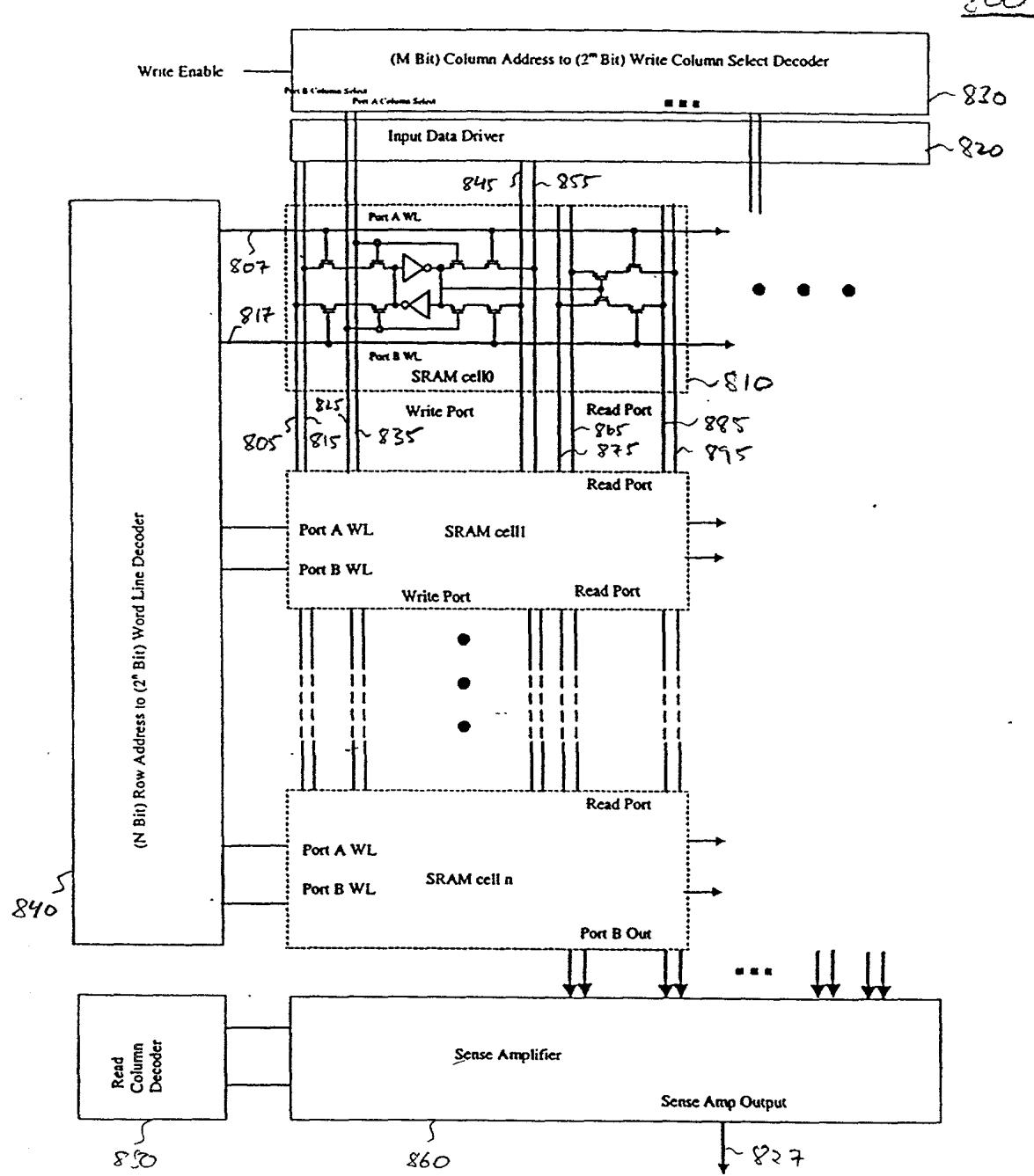


Figure 8

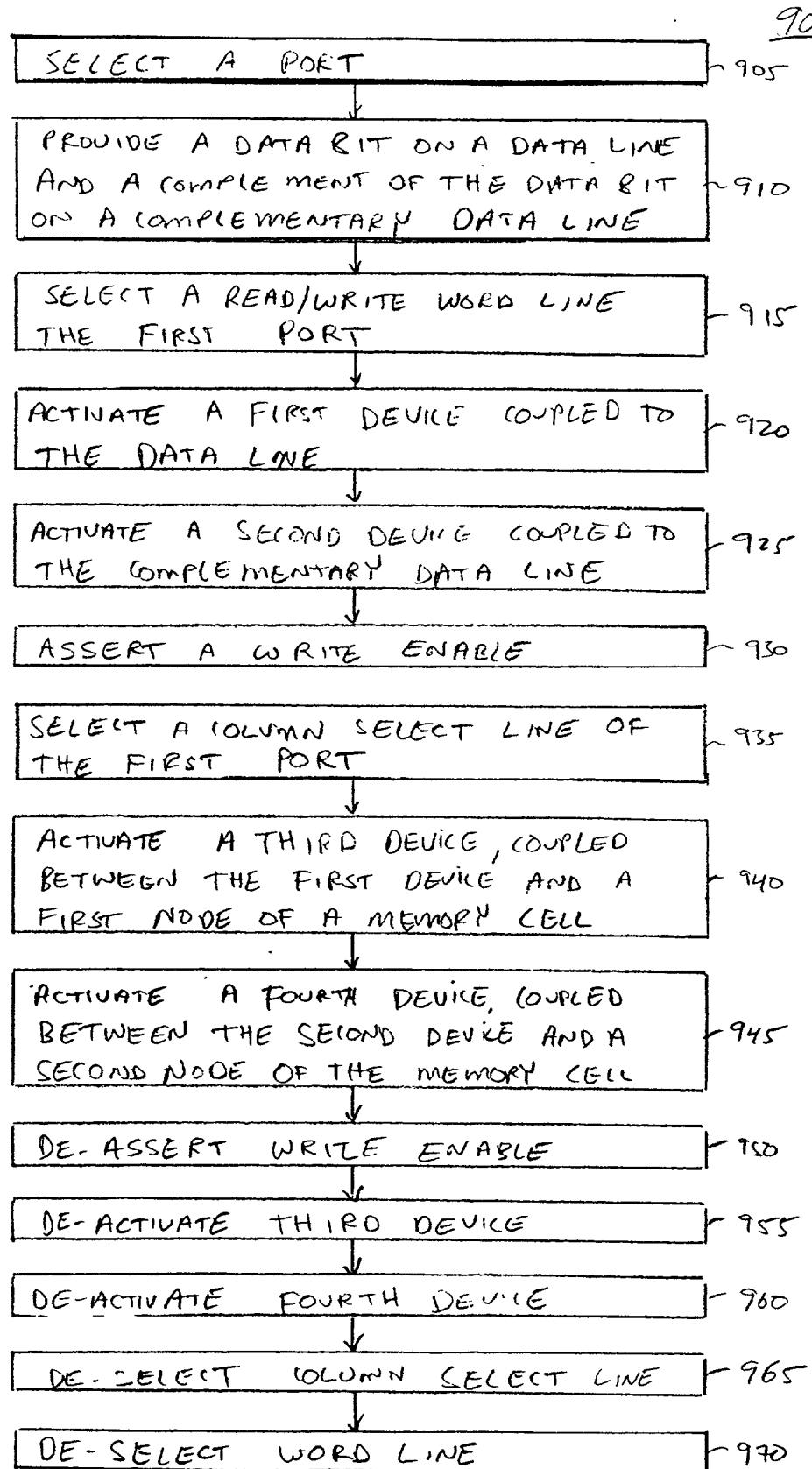


Figure 9

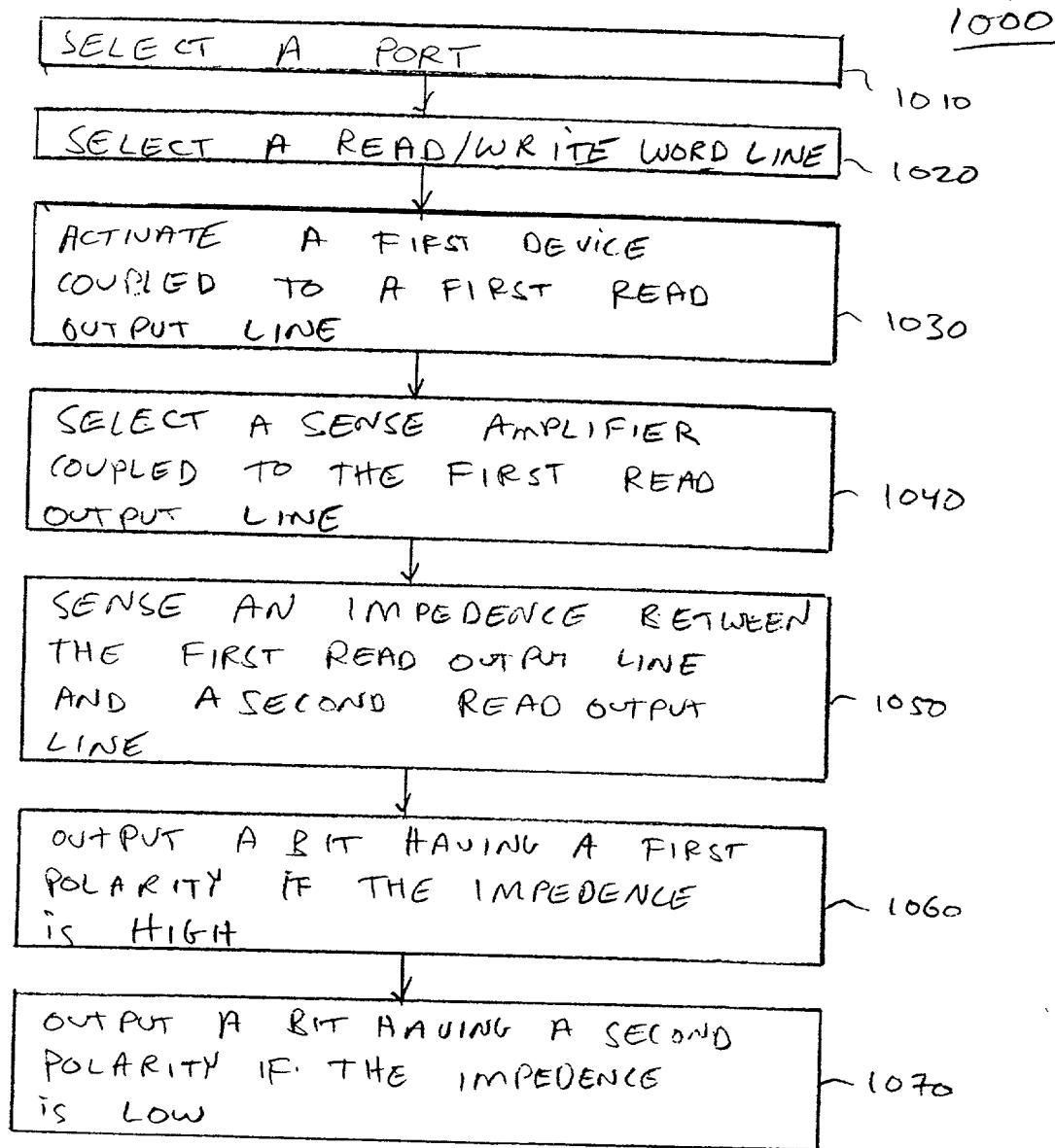


FIGURE 10

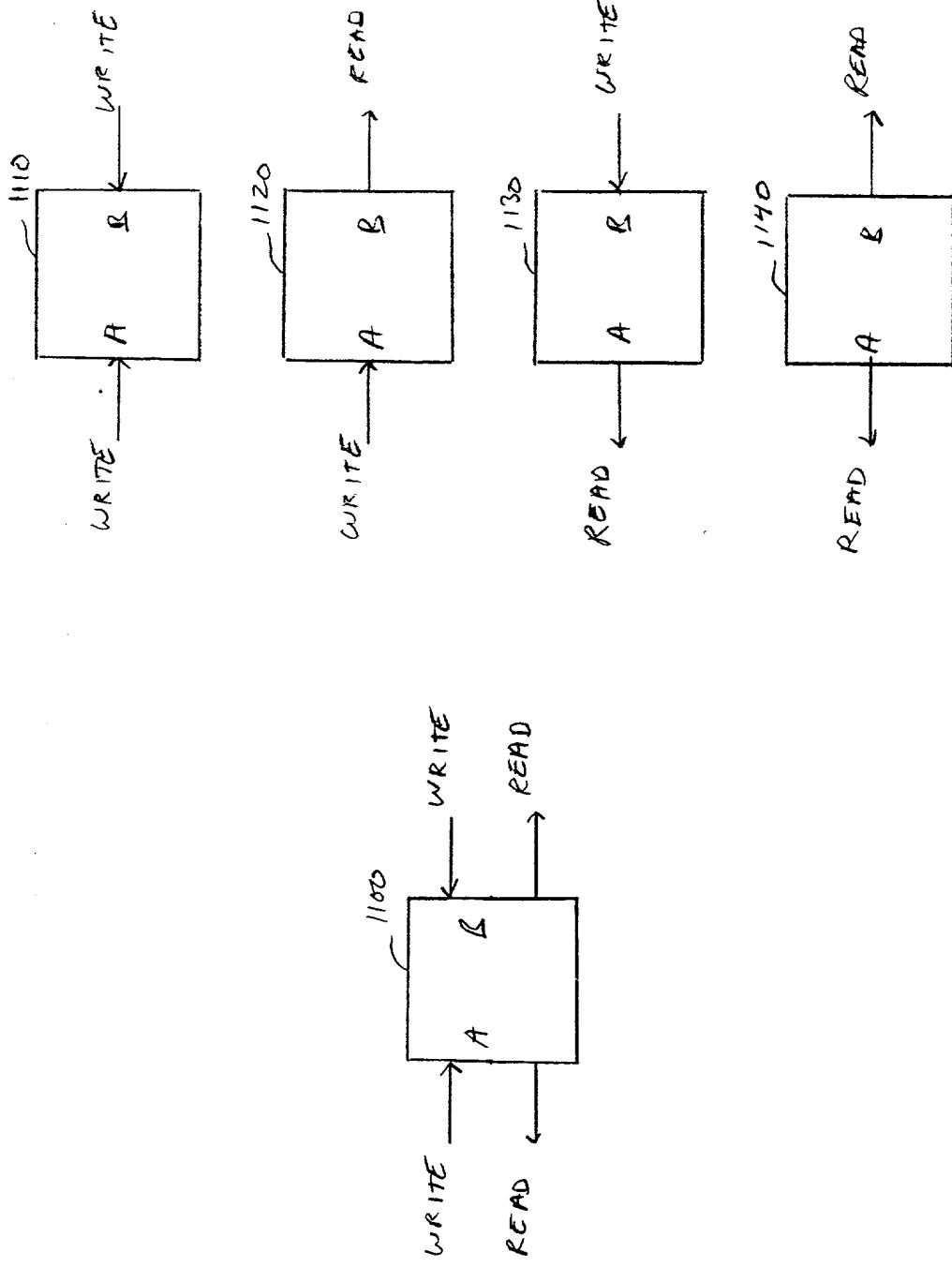


Figure 11

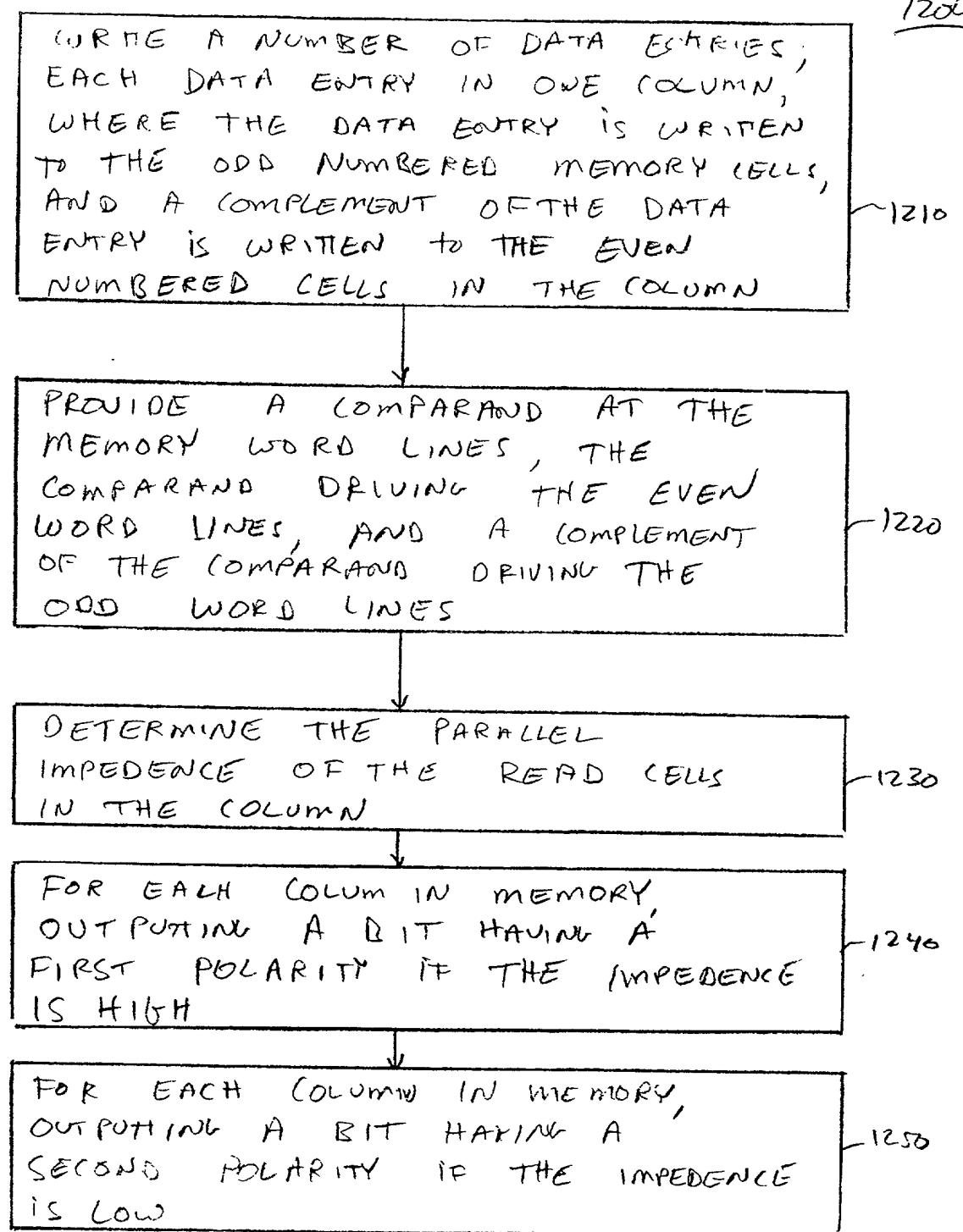
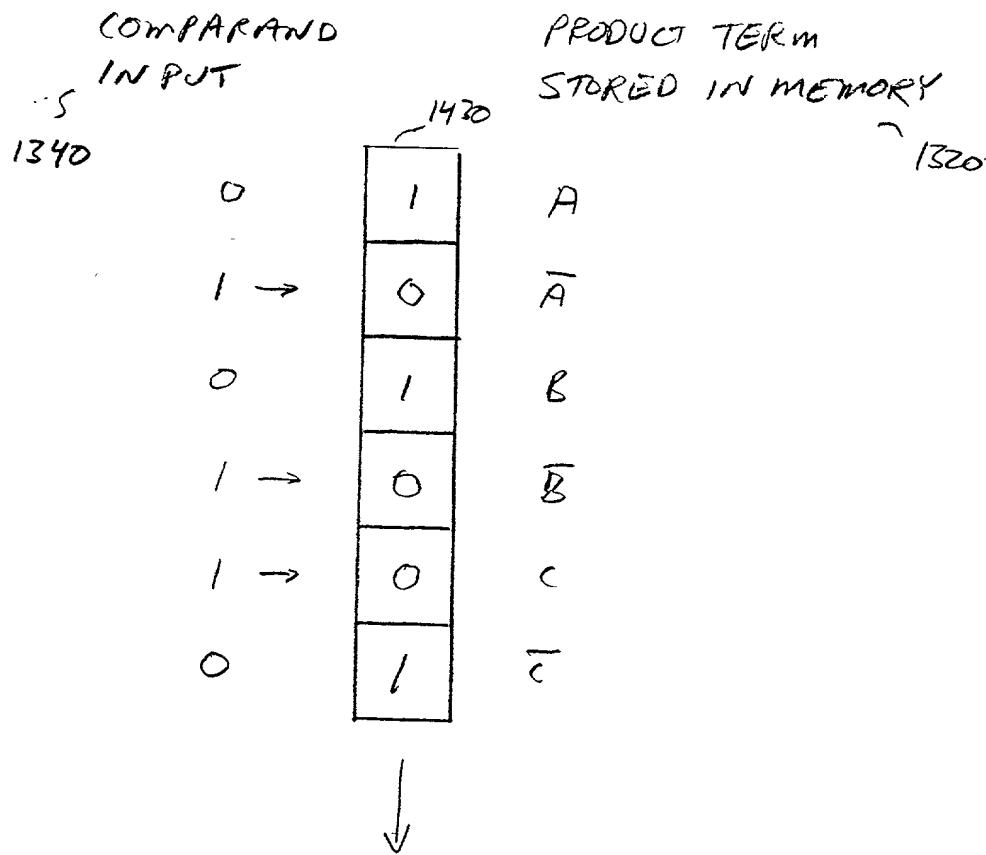


FIGURE 12

$$\text{PRODUCT TERM} = A \cdot \overline{B} \cdot \overline{C} \quad \overbrace{1300}^{1305}$$

1310 \hookrightarrow A "MATCH" OCCURS WHEN $A = 1, B = 1, C = 0$



HIGH IMPEDENCE AT ~ 1350
SENSE AMPLIFIER
INPUT

FIGURE 13

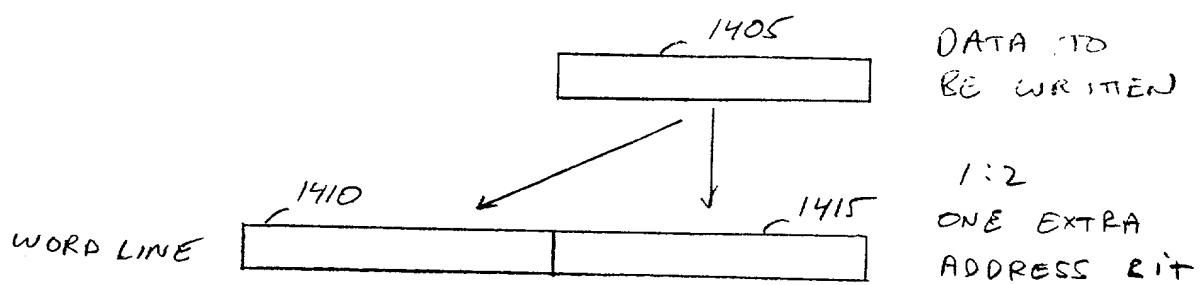


FIGURE 14A

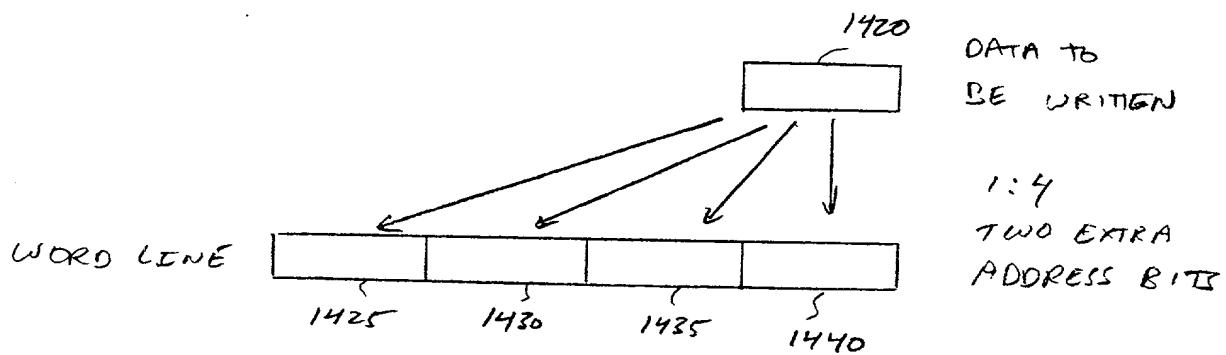


FIGURE 14 B

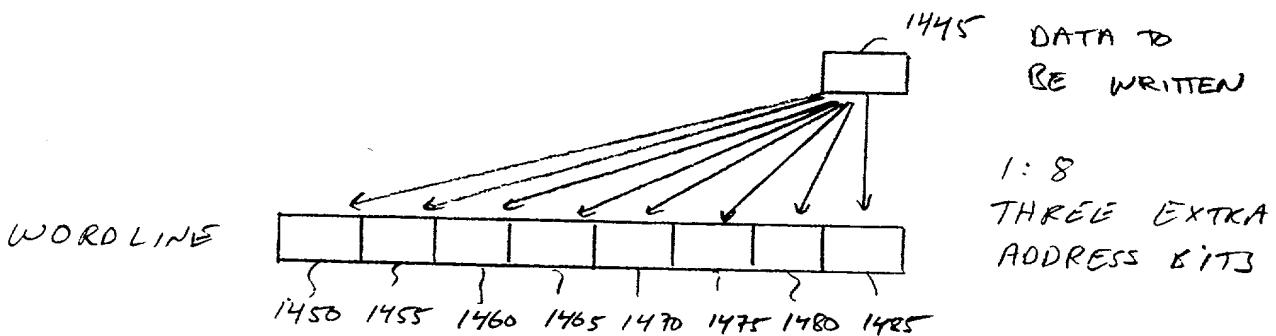


FIGURE 14 C

INPUT DATA BUS ~ 1520

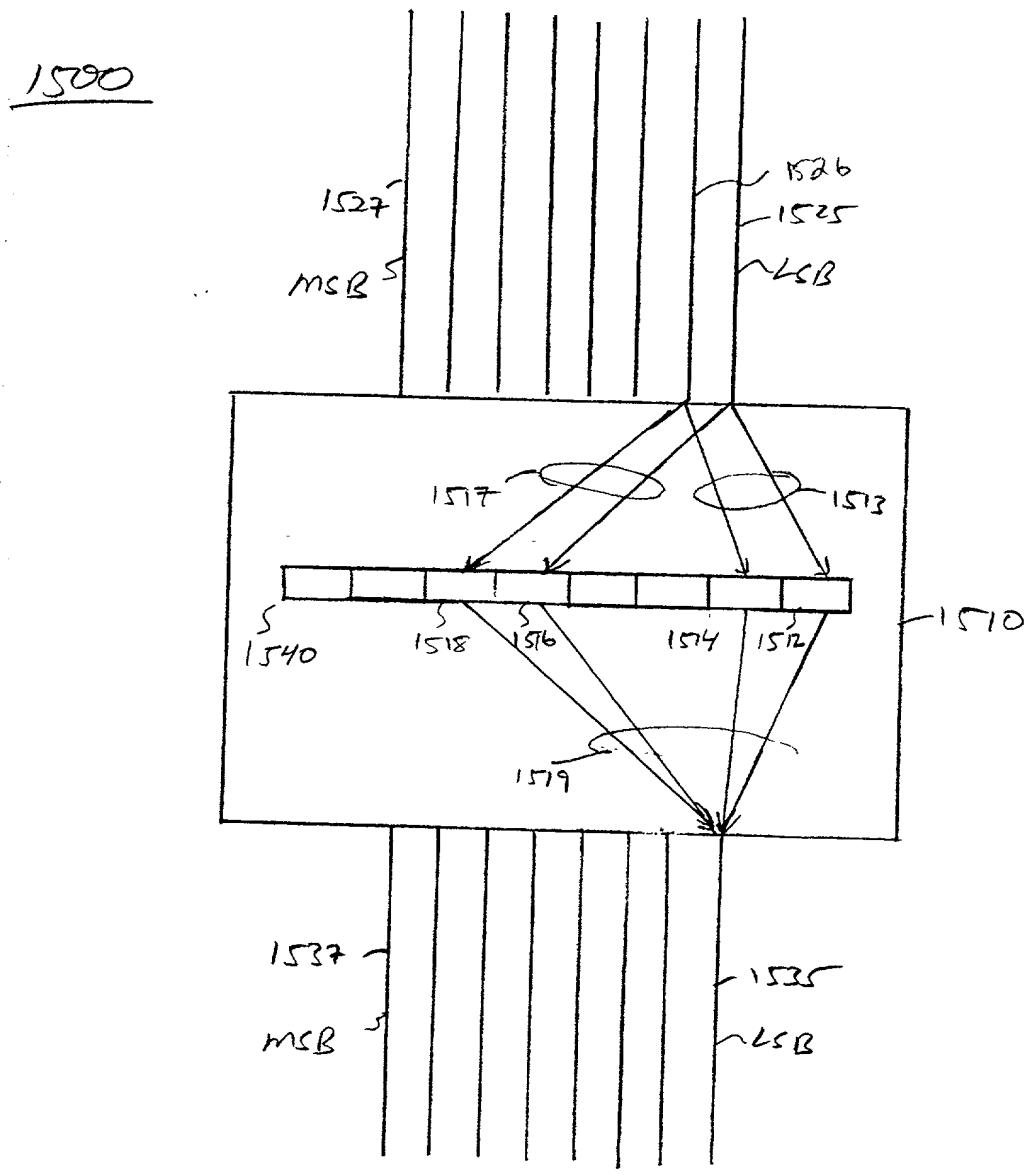


FIGURE 15

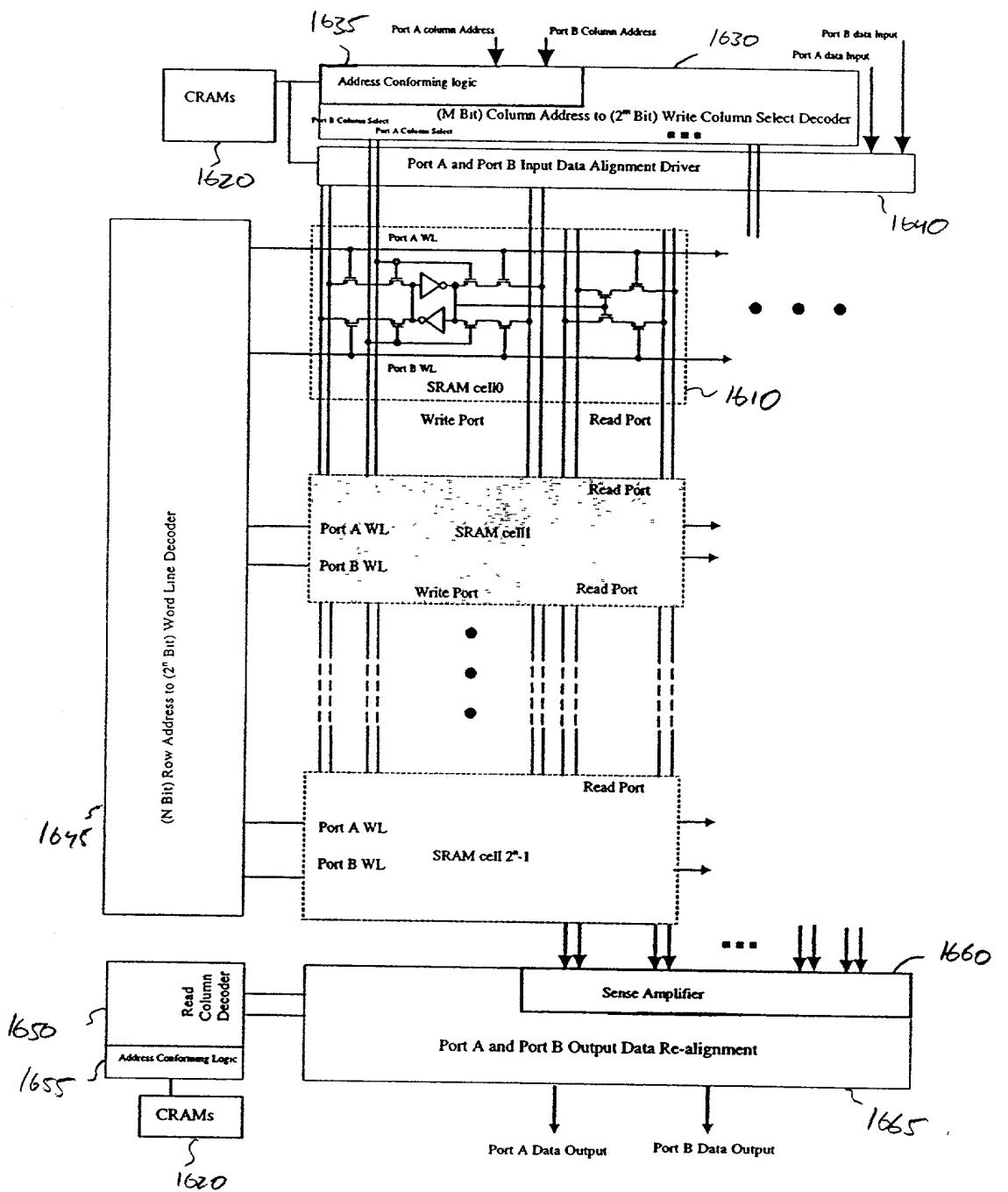
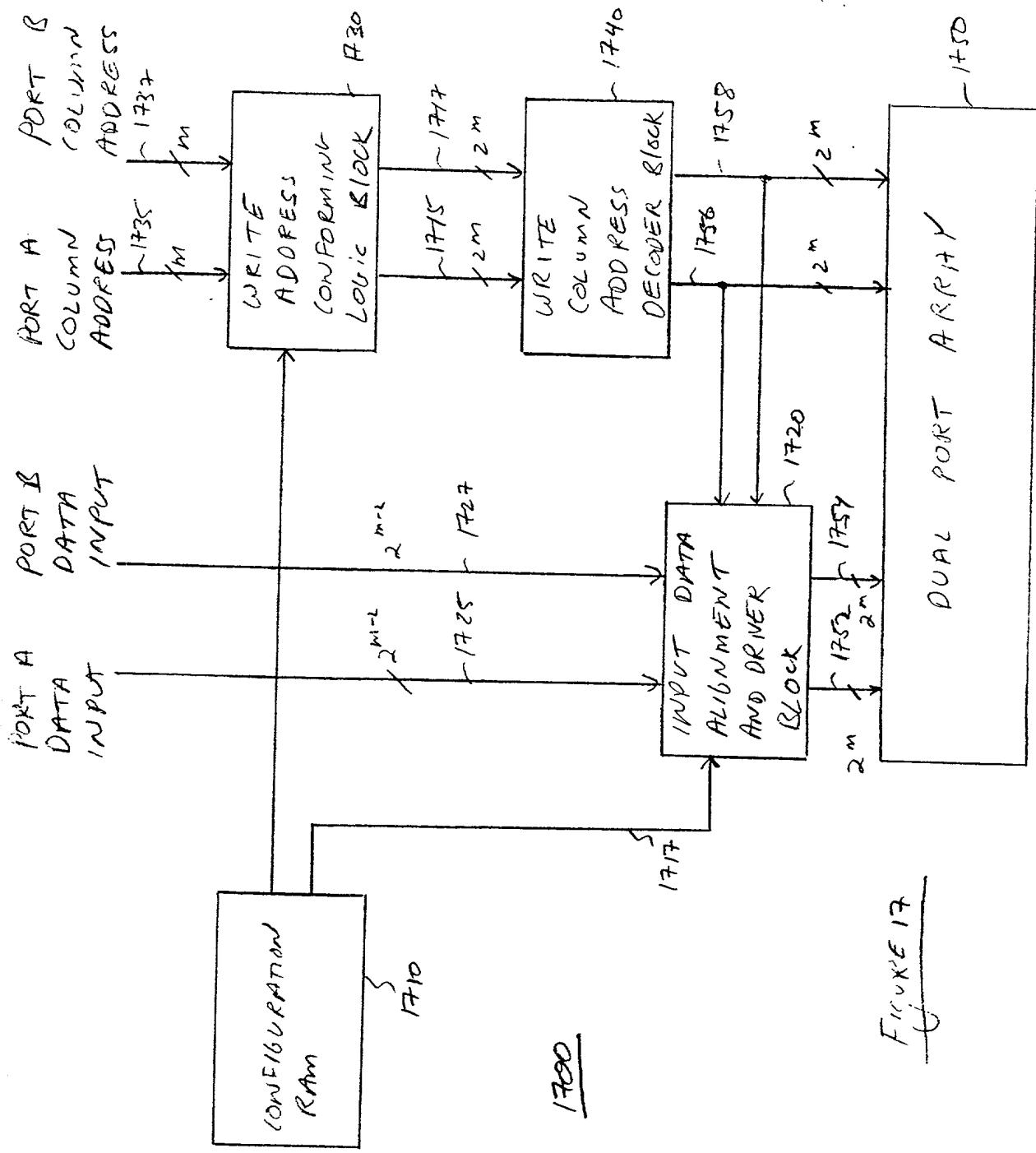


FIGURE 16



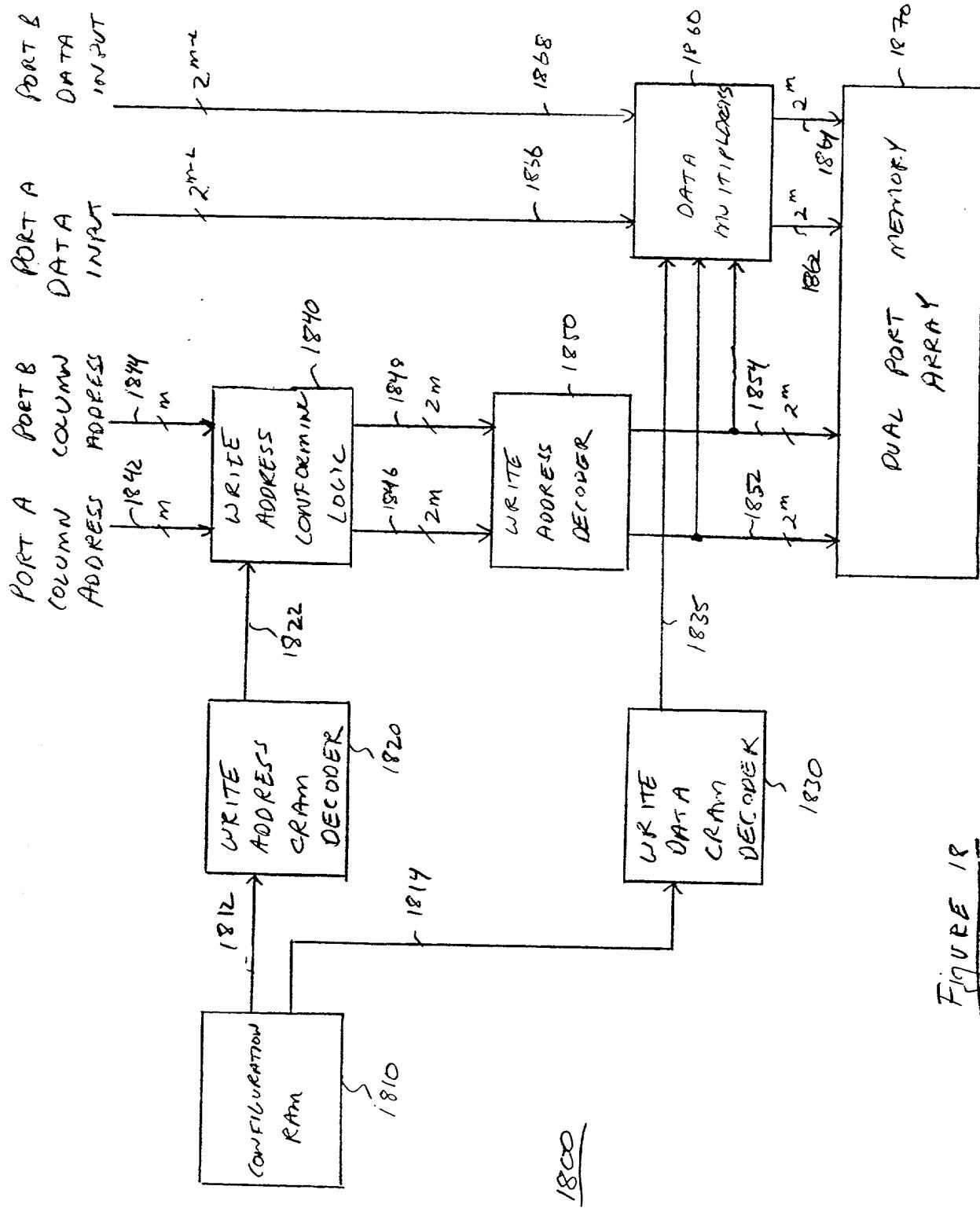


Figure 18

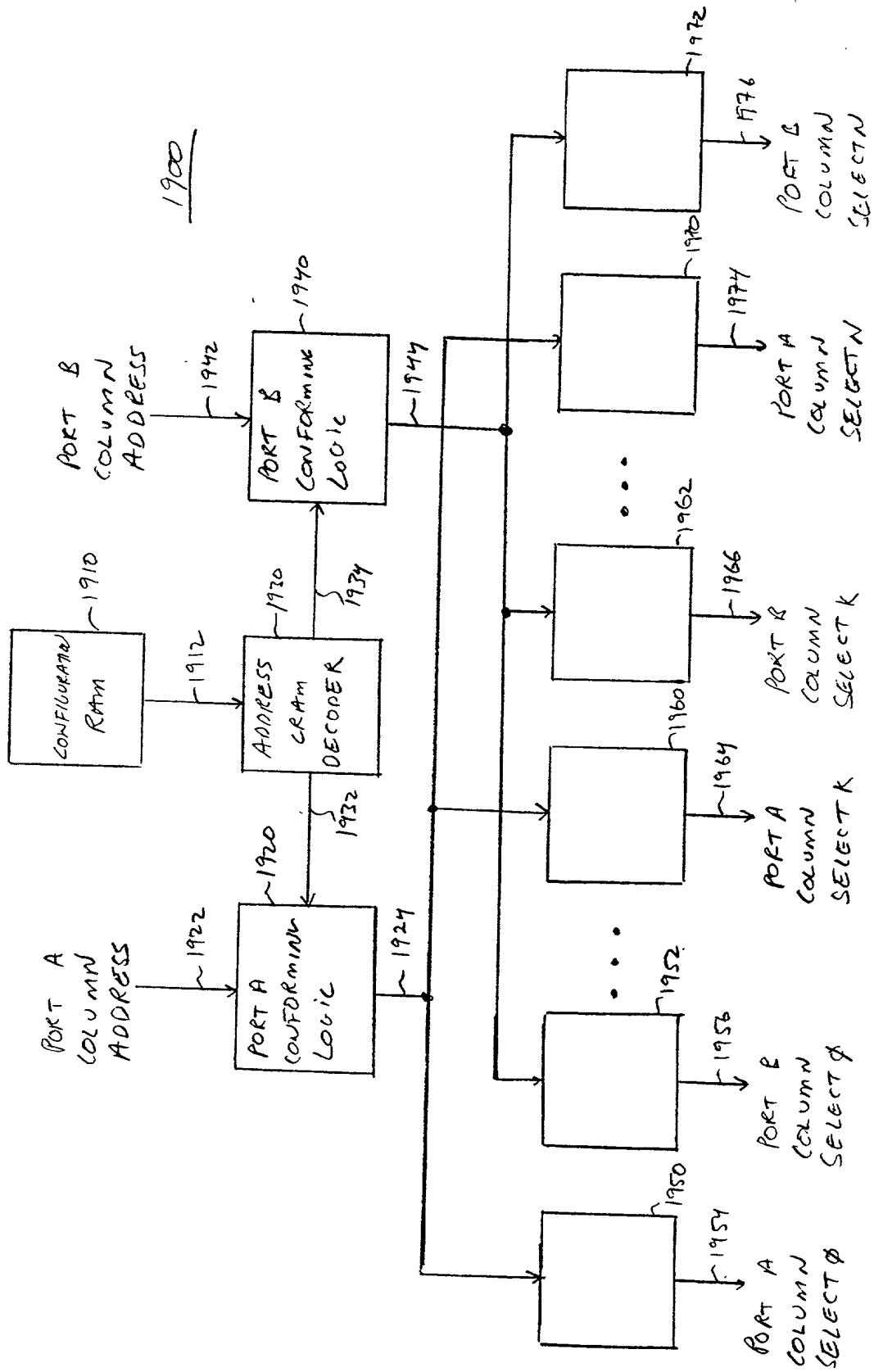


Figure 19

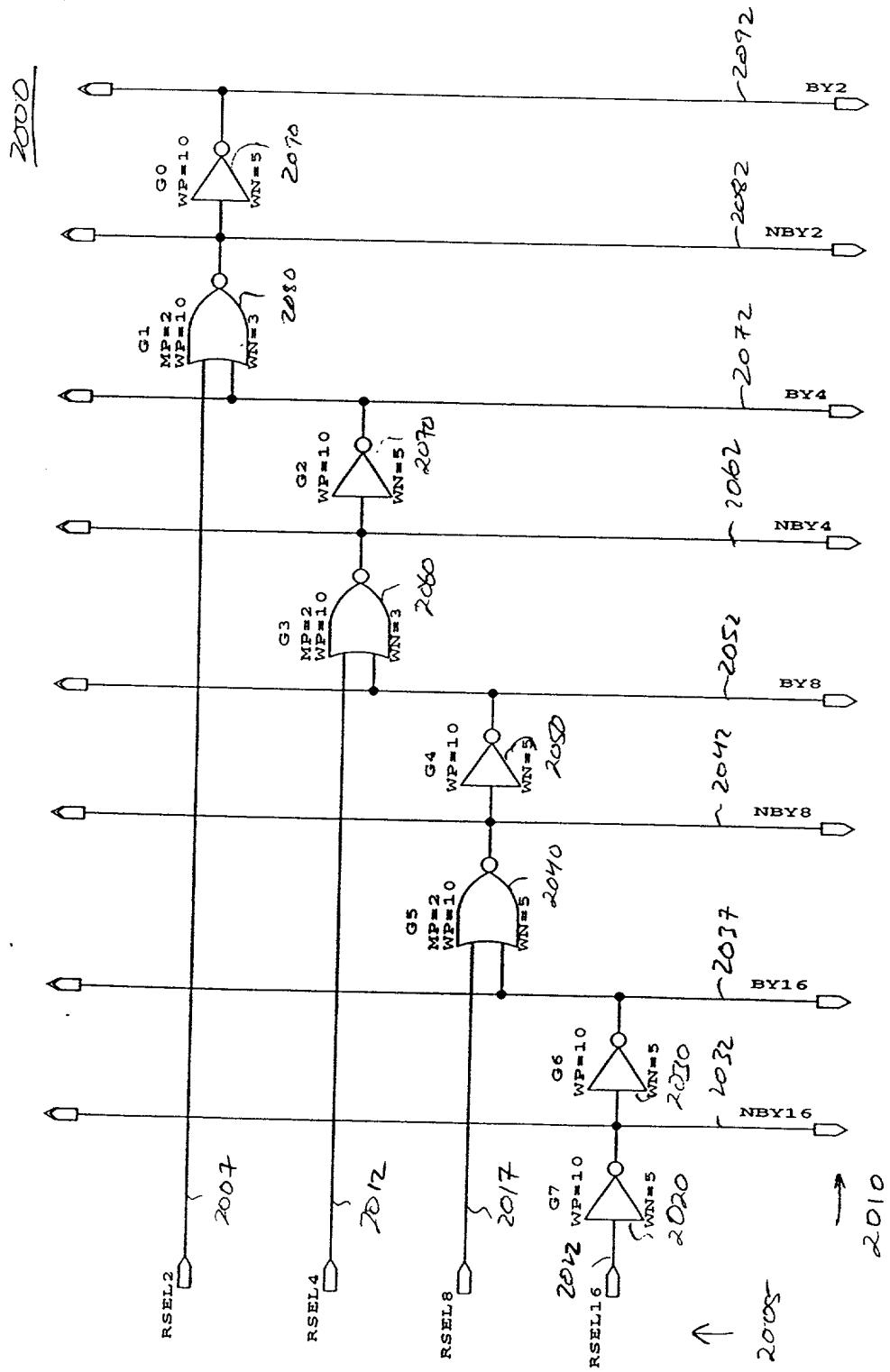


Figure 20

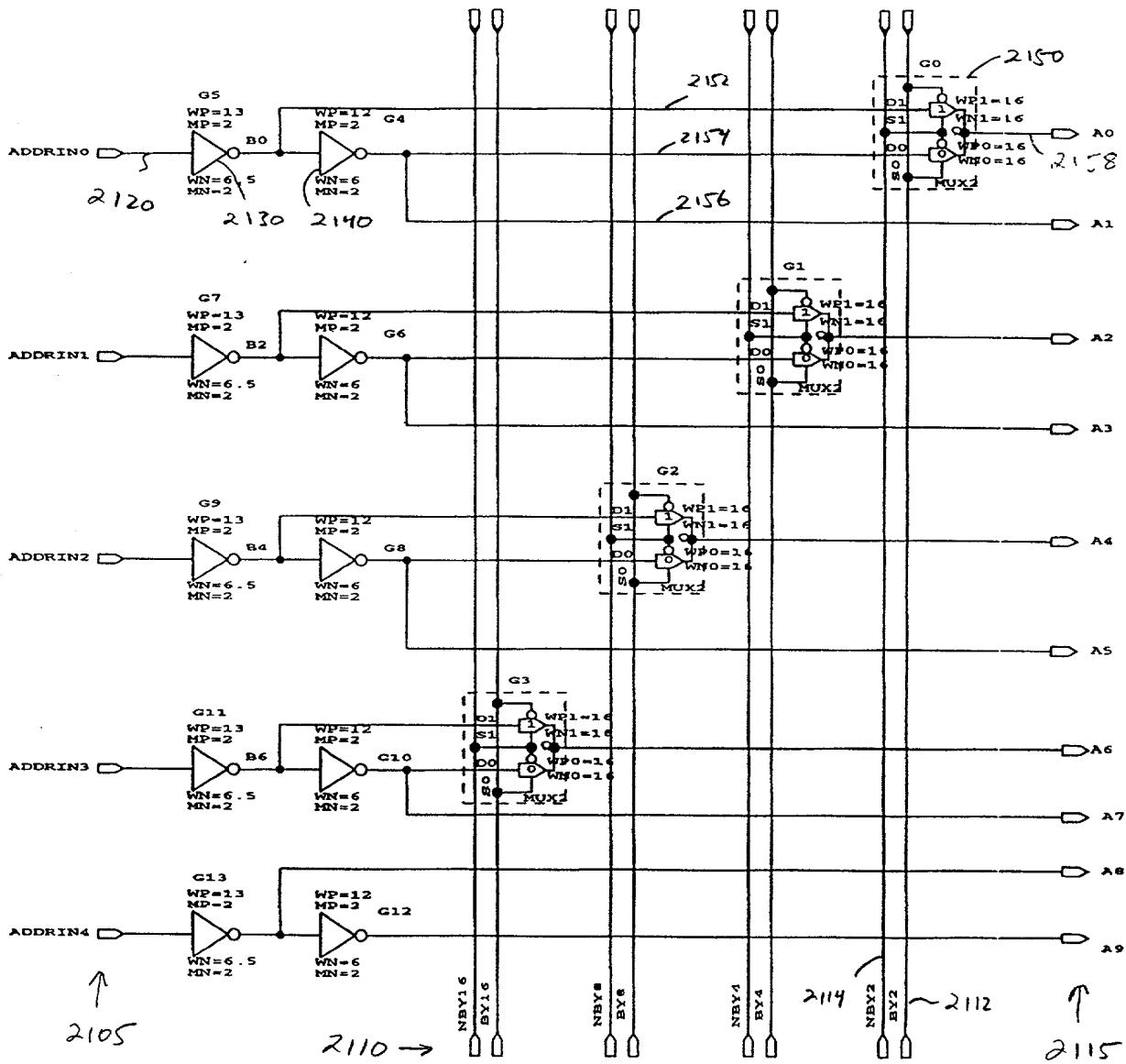
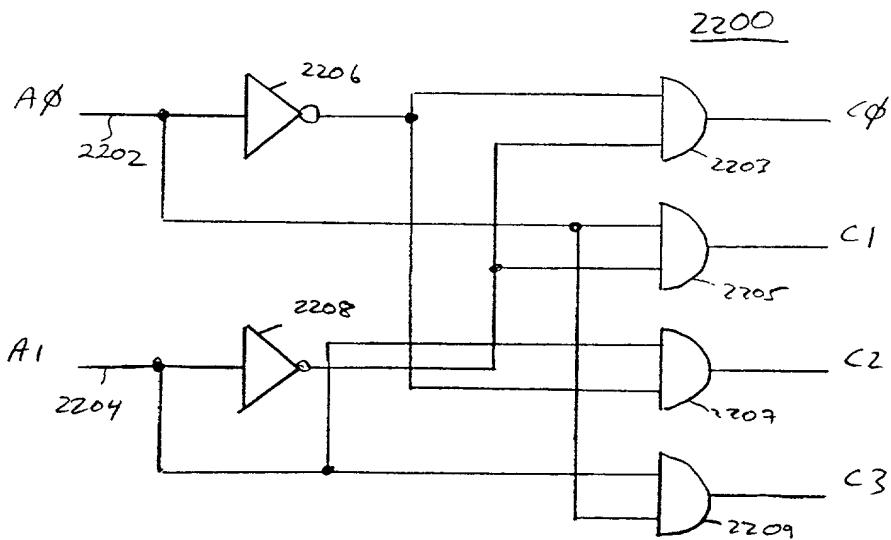


FIGURE 2)



2210

A_1	$A\phi$	$C\phi$	C_1	C_2	C_3
0	0	1	0	0	0
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	1

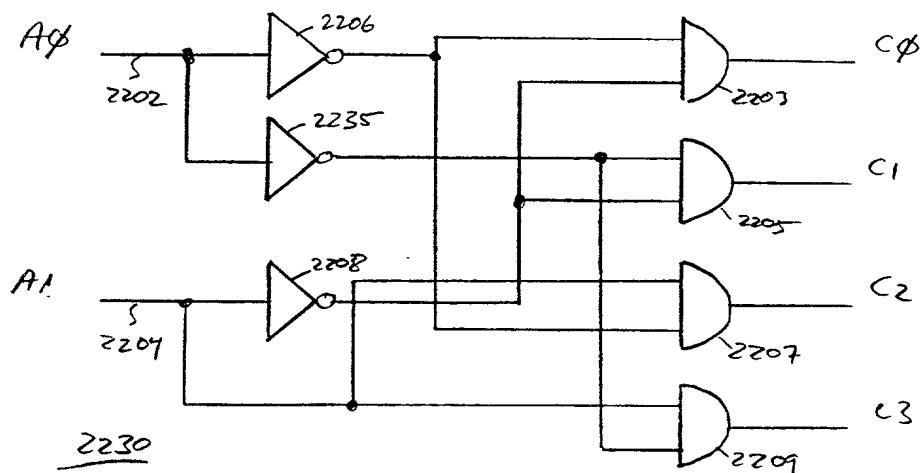
2220

$$C\phi = \overline{A_1} \cdot \overline{A\phi}$$

$$C_1 = \overline{A_1} \cdot A\phi$$

$$C_2 = A_1 \cdot \overline{A\phi}$$

$$C_3 = A_1 \cdot A\phi$$



2250

2240

$$C\phi = \overline{A_1} \cdot \overline{A\phi}$$

$$C_1 = \overline{A_1} \cdot \overline{A\phi}$$

$$C_2 = A_1 \cdot \overline{A\phi}$$

$$C_3 = A_1 \cdot A\phi$$

A_1	$A\phi$	$C\phi$	C_1	C_2	C_3	
0	0	1	1	0	0	~ 2252
0	1	0	0	0	0	~ 2254
1	0	0	0	1	1	~ 2256
1	1	0	0	0	0	~ 2258

FIGURE 22

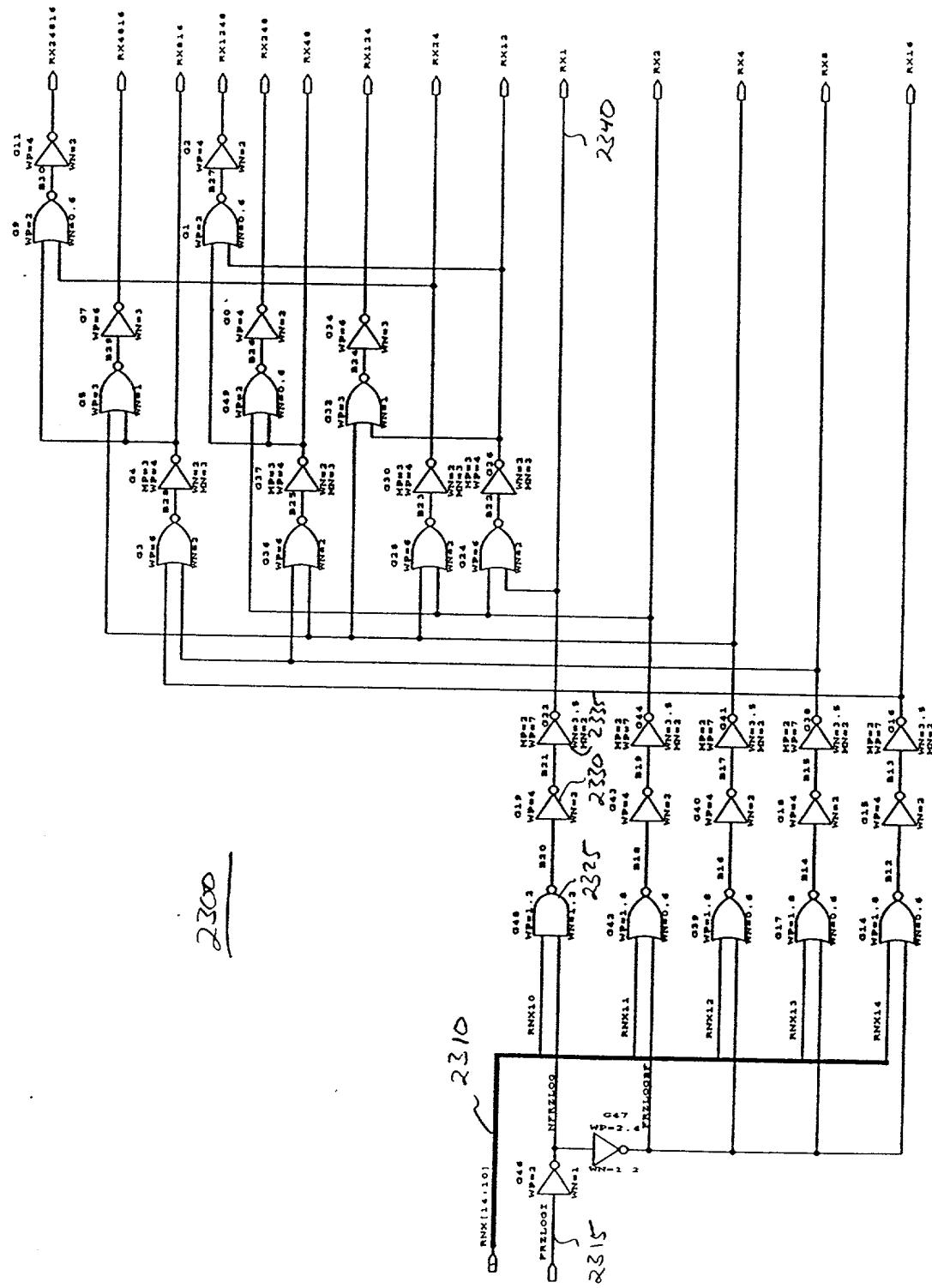
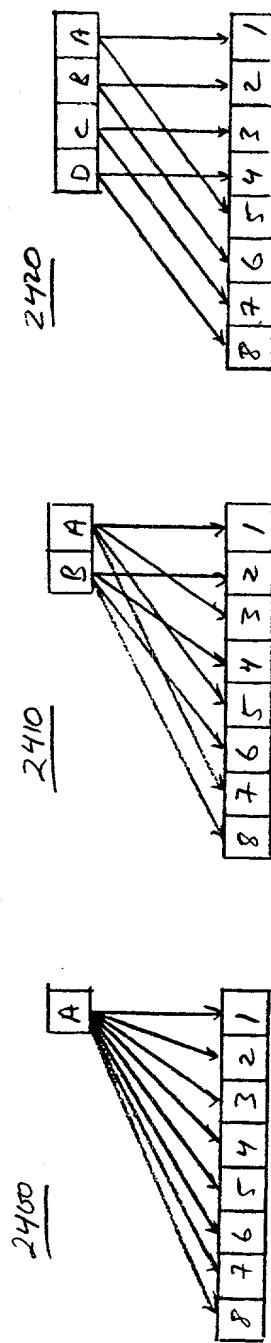


Figure 23



MUX	# bits/word	Inputs				TYPE mux
		1	2	4	7	
2432	~2434	A	A	A	A	1:1
	~2436	A	B	B	B	2:1
	~2438	A	A	C	C	2:1
		A	B	D	D	3:1
2430		A	A	A	A	1:1
		A	B	B	B	2:1
		A	A	C	C	2:1
		A	B	D	D	3:1
2440	~2444	Outputs				~2446
	~2442	Outputs				TYPE mux
		A	8, 7, 6, 5, 4, 3, 2, 1			1:8
		B	8, 7, 6, 5, 4, 3, 2			1:7
		C	7, 3			1:2
		D	8			1:1

Figure 24

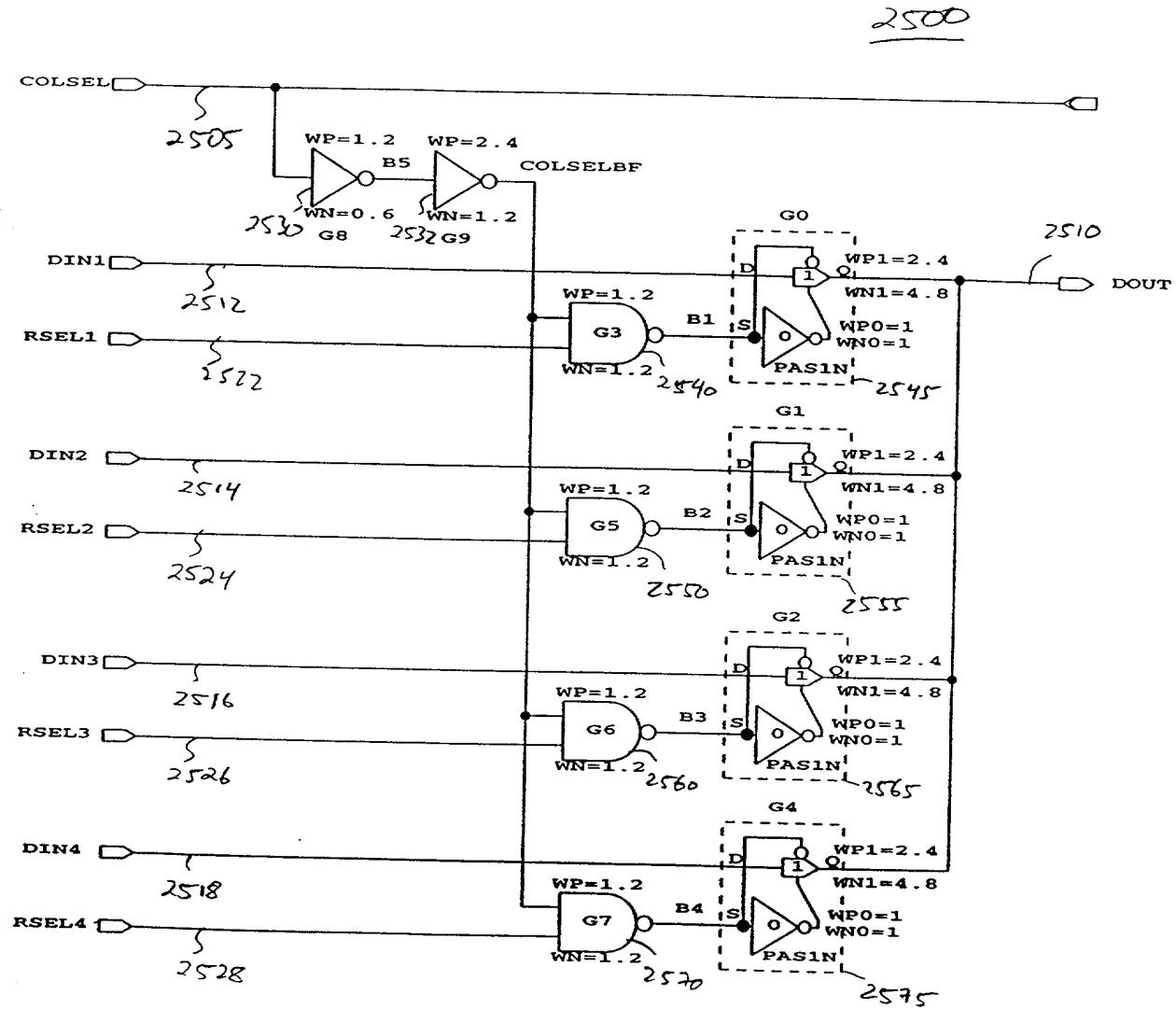


Figure 25

2600

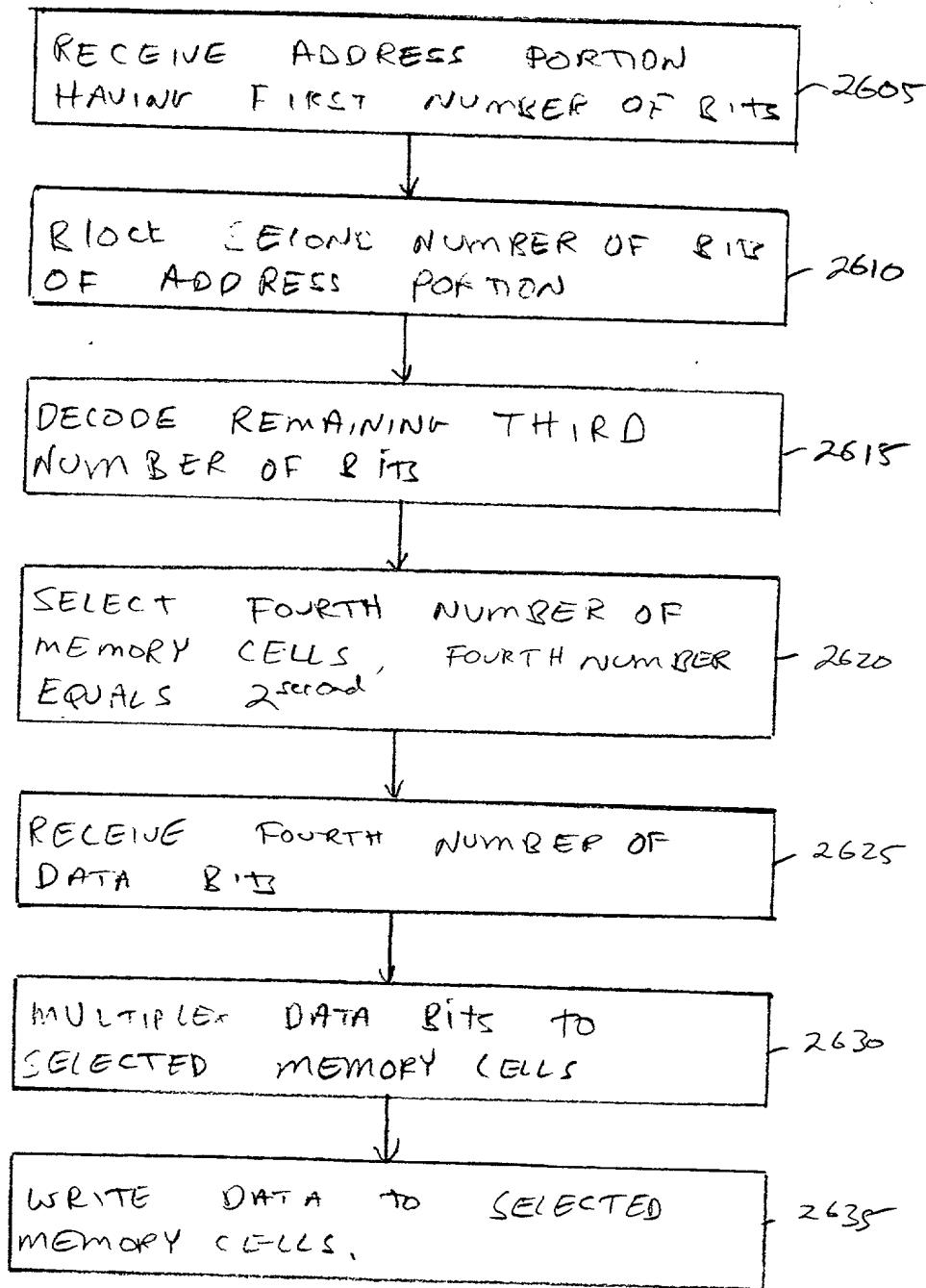


FIGURE 26

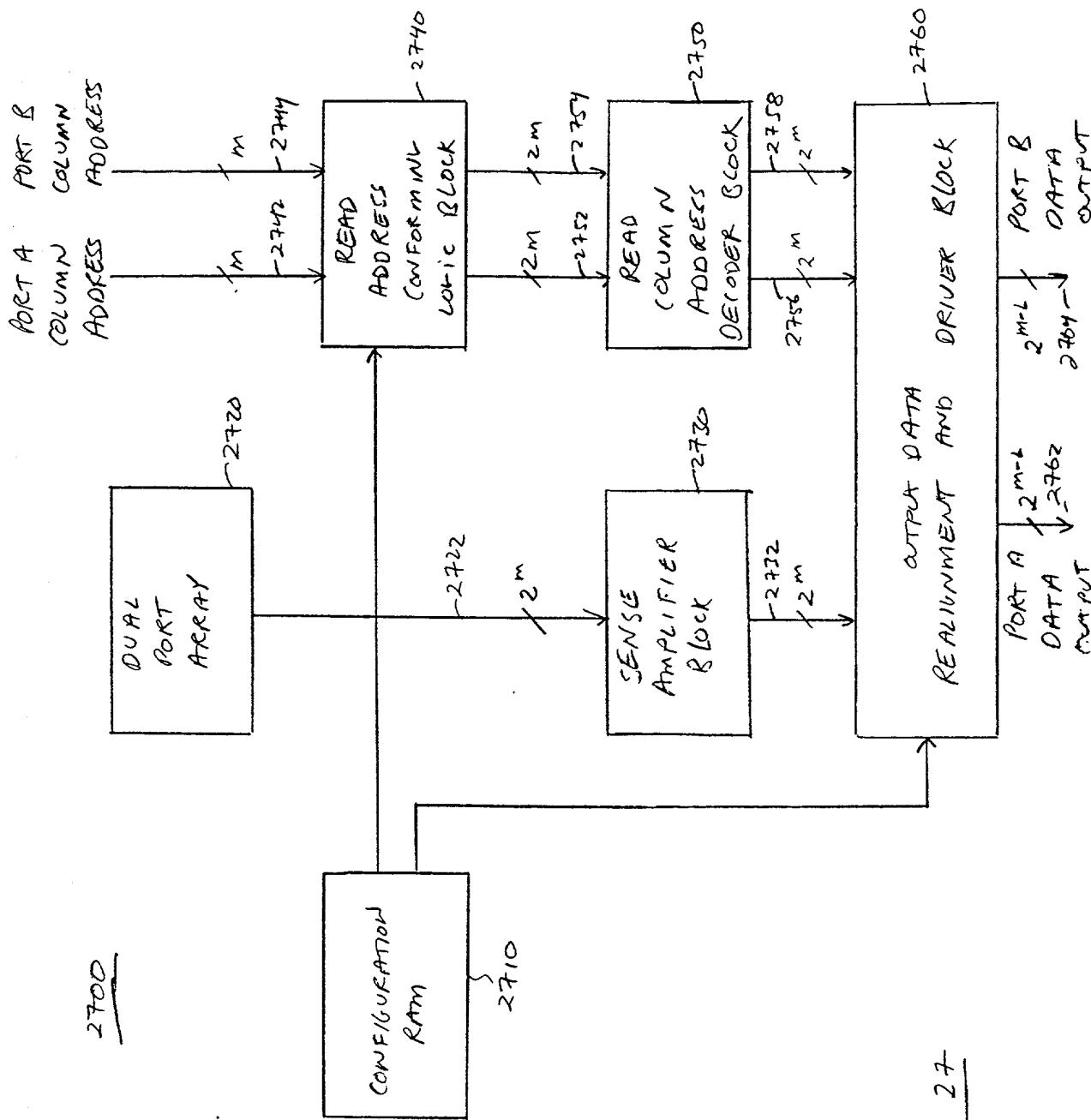


Figure 27

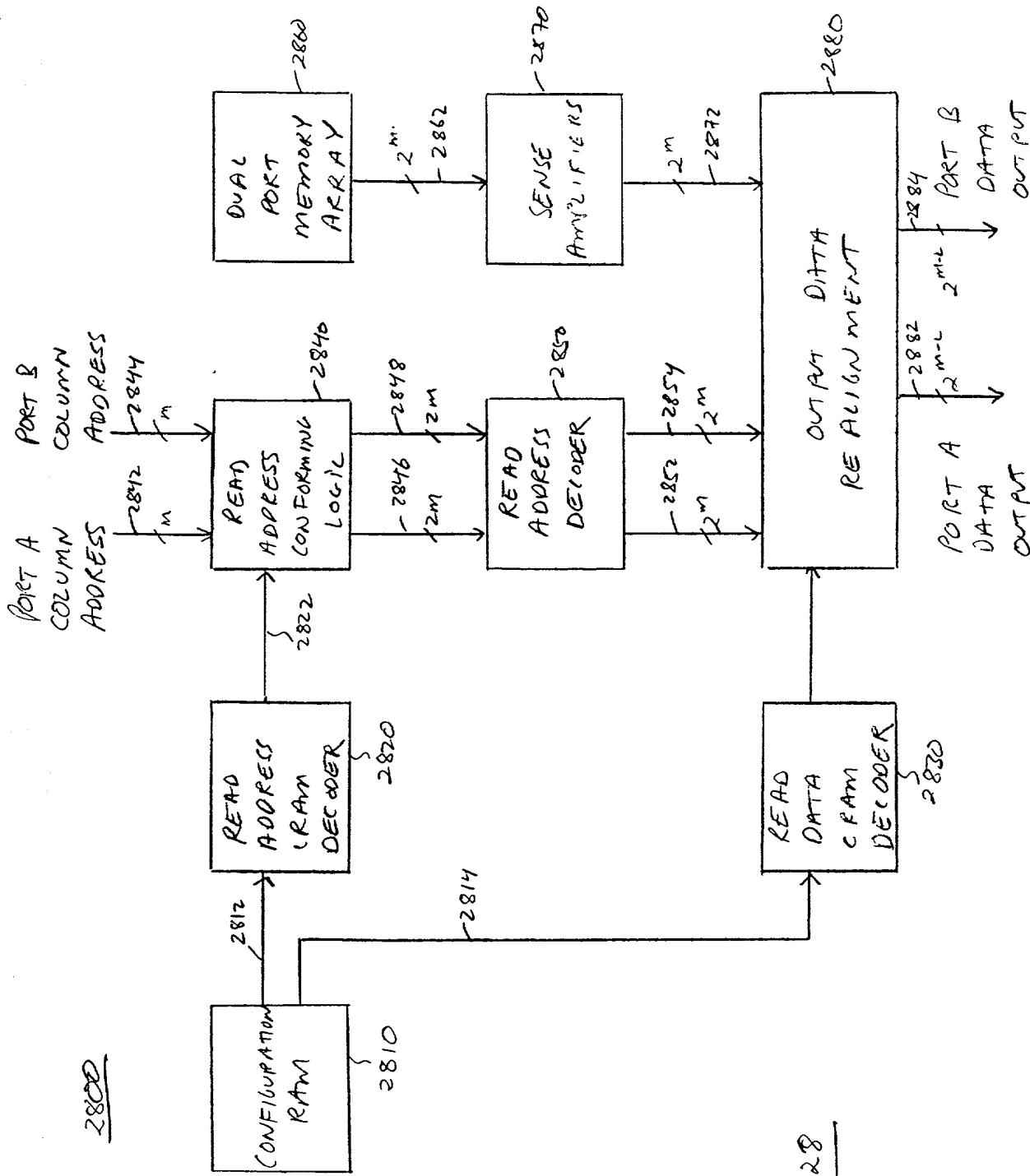
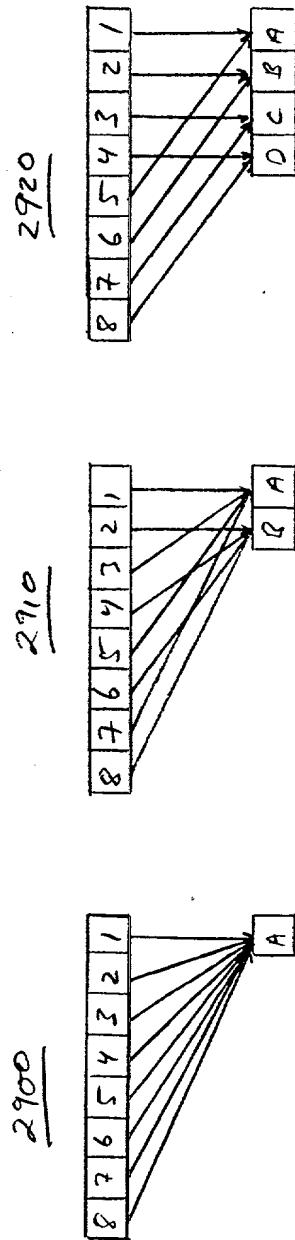


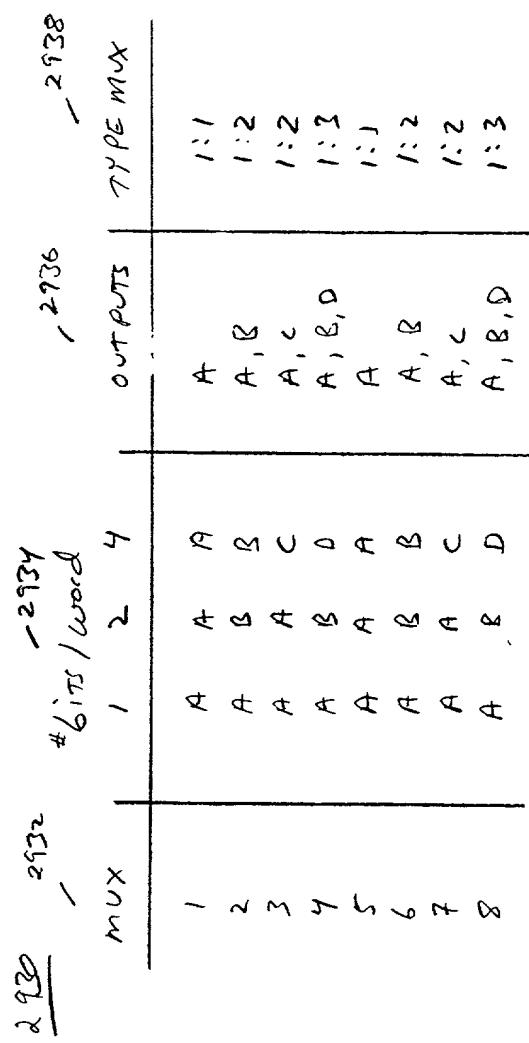
FIGURE 28



2920

2910

2900



2932 2932 #675 / word -2934

— 2938
— 2936

MUX	INPUTS	TYRE mix	
-2992	-2994	-2996	
A	8, 7, 6, 5, 4, 3, 2, 1	8:1	
B	8, 6, 4, 2	4:1	
C	7, 3	2:1	
D	8	1:1	

2940

2946
TYRE mix

Figure 29

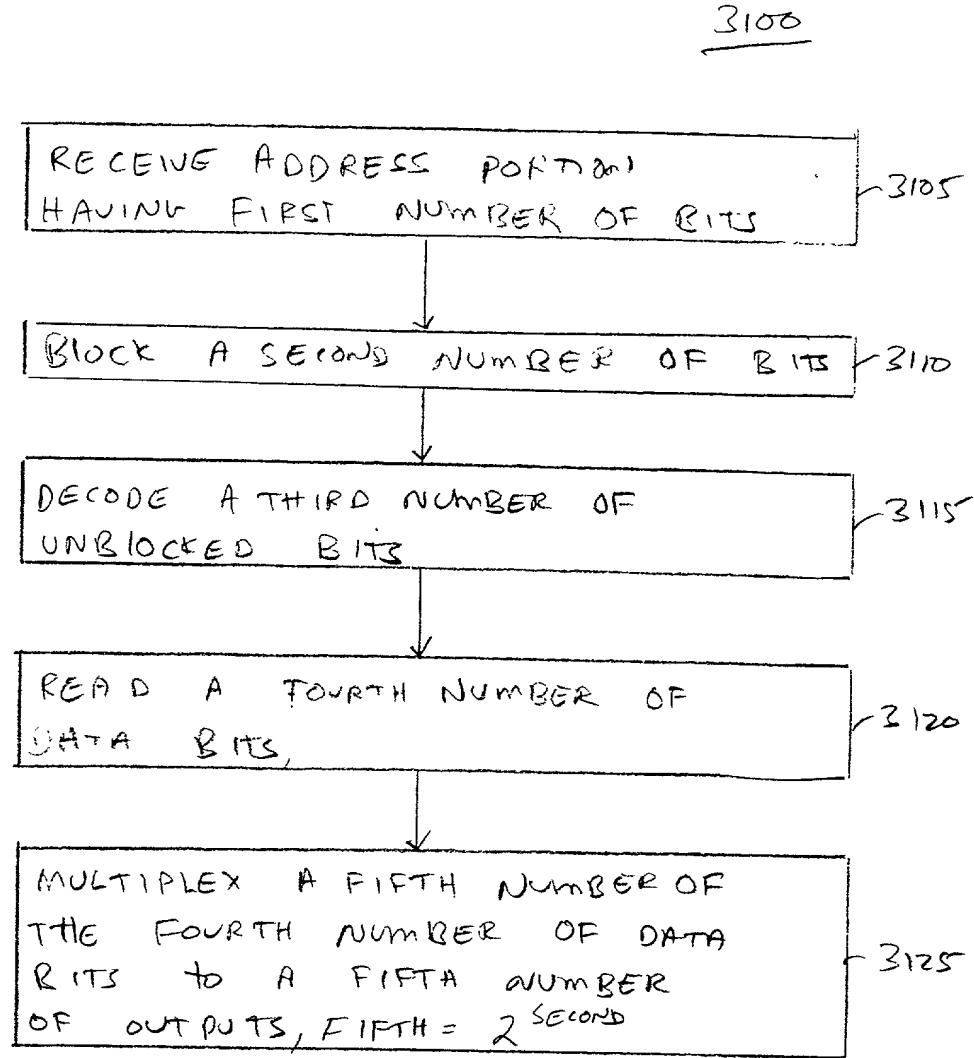


FIGURE 31